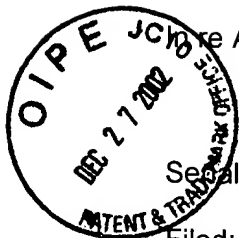


#2
BT
2183
01-06-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Re Application of:

V. Alves et al.

Serial No.: 09/888,838

Filed: June 25, 2001

Group No.: 2183

Examiner: Unknown

Docket No. MORPH1160

For: **METHOD AND APPARATUS FOR EXECUTING A CRYPTOGRAPHIC ALGORITHM
USING A RECONFIGURABLE DATAPATH ARRAY**

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date 19 December 2002, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

19 December 2002
Date

Terrance A. Meador
Signature

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

RECEIVED

DEC 31 2002

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Applicant hereby cites the documents listed in accompanying Form PTO-1449 with respect to the above-referenced patent application under the provisions of 37 CFR 1.97(b). Copies of the documents are attached.

The undersigned hereby certifies that each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement.

The Examiner is respectfully requested to make the listed documents of record in connection with the prosecution of the subject application.

Respectfully submitted,

Terrance A. Meador

Date: 19 December 2002

TERRANCE A. MEADOR
Registration No. 30,298

GRAY CARY WARE & FREIDENRICH
4365 Executive Drive, Suite 1100
San Diego, CA 92121-2133

Telephone: (858) 638-6747 Fax: (858) 638-6727

Please type a plus sign (+) inside the box ☒

PTO/SB/08B (08-00)

Approved for use through 10/31/2002. OMB 0651-003

U.S. Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets necessary)		COMPLETE IF KNOWN			
		Application Number	09/888,838		
		Filing Date	June 25, 2001		
		First Named Inventor	V. Alves et al.		
		Group Art Unit	2183		
		Examiner Name	Unknown		
SHEET	1	OF	1	Docket Number	MORPH1160

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
	1.	US-2002/108063		Kurdahi et al.	08-08-2002	

RECEIVED
DEC 31 2002

Technology Center 2100

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
	2.	NOTIFICATION OF TRANSMITTAL OF INTERNATIONAL SEARCH REPORT and INTERNATIONAL SEARCH REPORT for PCT/US02/20238, dated 12/12/02, 3 pg.	
	3.	HAUSER, J.R. et al., "Garp: A MIPS Processor with a Reconfigurable Coprocessor," Field-Programmable Custom Computing Machines, 1997, 5th Annual IEEE Symposium in Napa Valley, CA April 16-18 1997, IEEE Comput. Soc., April 16, 1997, pages 12-21	
	4.	3GPP TASK FORCE, "Specification of the 3GPP Confidentiality and Integrity Algorithms - Document 2 Kasumi Specification", Version 1.0, Retrieved from the internet: <URL: http://www.etsi.org/dvbandca/3Gpp/3Gppconditions.html >, pages 8-20	
	5.	HAYNES, S.D., et al., "Configurable Multiplier Blocks for Embedding in FPGAs," Electronic Letters, IEEE Stevenage, GB, Vol. 34, No. 7, April 2, 1998, pages 638-639	

Examiner Signature	Date Considered
--------------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two letter-code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard St. 16 if possible. ⁶Applicant is to place a check mark here if English language translation is attached.